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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Tsutomu MEZAWA

Serial No.: New Application

Group Art Unit:

Filed: July 22, 1998

Examiner:

For: INTEGRATED CIRCUIT DEVICE MODULE

S-11-98

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents
Washington, D. C. 20231

July 22, 1998

Sir:

In compliance with applicant's duty of disclosure under 37 C.F.R. 1.56, the following is information of which applicant is aware and which the Examiner may consider to be important in the examination of the above-identified application:

Japanese Patent Publication No. 4-221830
with English Abstract thereof

A copy of the above-listed information is attached hereto this paper.

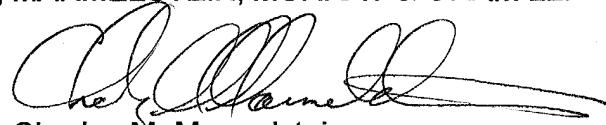
The above-listed information is presented so that the Patent and Trademark Office can, in the first instance, determine any materiality thereof to the claimed invention. See 37 C.F.R. 1.104(a) and 1.106(b) concerning the Patent and Trademark Office duty to consider and use any such information. It is respectfully requested that the information be

expressly considered during the prosecution of this application, and that the document cited in the attached Form PTO-1449 be made of record therein and appear on the first page of any patent to issue therefrom.

In the event that any fees are due with this paper, please charge our Deposit Account No. 14-1060.

Respectfully submitted,

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Atty. Docket No.: P8066-8017

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Enclosures: Form PTO-1449, Reference (1)